AN INTRODUCTION TO THE WORKSHOP AND THE PROGRAMME FOR THE FIRST DAY

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Welcome to this workshop on the work of a TEMPUS project, Electronic design education and research into VLSI design and image processing. As a result of changes in the timing of the TEMPUS funding and the successful MICORCOMPUTER School in September 1991, which was co-sponsored by this TEMPUS programme, it was decided that a specialist workshop would be arranged which would be aimed at disseminating research results and teaching activities from the west and the east at a venue that Eastern European senior and junior staff could attend. In summary the aims of the workshop are to:

- attract an invited audience from East European countries,
- present the work of the TEMPUS JEP 0449 project,
- present research associated with the TEMPUS project,
- invite presentations from East European academics,
- promote wider use of the work of the TEMPUS project ,
- offer the opportunity for young lecturers to attend ,
- to promote an electronic CAD initiative in Poland.

The programme was designed with these key aims in mind. The subjects reflect the key activities of the TEMPUS project which are, as well as supporting substantial student and staff mobility, to develop curriculum materials and acquire equipment in the areas of:

- Advanced computer architectures
- Image processing and computer vision
- ASIC design and CAD

From these subjects four themes were identified for each of the four days of the workshop. These are:

- An Electronic CAD initiative for Poland
- Image processing research
- The ACEP TEMPUS programme
- VLSI design research

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A proceedings of the workshop is to be produced and will be available later in the year. This will be published by a Polish press.

The speakers have been drawn mainly from the TEMPUS consortium and these people will speak of their research, their experiences on TEMPUS activities or their work in developing curriculum material. Other invited speakers are here to address specific topics within the programme. In particular I am pleased to say that we have Mr Jerzy Dalek, who is Director of IT at the Ministry of National Education. He is particularly interested in the discussion on the use of Electronic Design in Education and the EUROCHIP and ECAD initiatives which provide a wide range of ECAD tools for Europe and the UK respectively. In the same discussion I am also pleased to welcome Dr Klaus Woelcken of the CEC, ESPRIT Office, Brussels who has masterminded the EUROCHIP initiative after working on a National scheme in Germany. These two speakers will be with us only for the one day.

The other speakers at the workshop from the West are:

- Prof. Erik Dagless, Dr. Dave Milford, Dariusz Caban, Ray Satchell, University of Bristol
- Wojciech Zamojski, (Dariusz Caban) Wroclaw Technical University
- Marian Adamski, The Higher College of Engineering in Zielona Gora
- Peter Hicks, Trevor York UMIST Manchester
- Antonio Ferrari, University of Aveiro
- Alberto Proenca, University of Minho Braga
- Andy Duller, University College of North Wales (proposed new member)
- Peter Jones, University of Manchester

These people will be introduced as the programme proceeds. To complement these speakers there are a number of invited speakers from Polish institutions. I am convinced that these speakers and the audience will make this meeting a memorable and happy event and I look forward to meeting and speaking with you during the proceedings.

The first day of the programme is devoted to the matter of a VLSI design initiative for Poland. The papers in this session have been carefully selected to provide a good overview of experiences in countries in Europe, and to guide the discussion onto the particular problems and challenges that have to be faced in pushing forward with a similar scheme in Poland. The programme for the start of the day aims to set out the background and is as follows:

- A VLSI Design Initiative EUROCHIP, i.e. an ESPRIT VSLI Design action for Europe
- The UK ECAD Initiative, i.e. scheme to make Electronic CAD available for <u>all</u> UK Higher Education Institutions
- Experiences of a EUROCHIP Member
- Panel Discussion

After this there will be an introduction by Jerzy Dalek on the particular issues that have to be faced in promoting a scheme for Poland which is then followed by papers reporting the experiences of teaching VLSI design and using the sort of complex software needed to keep at the forefront of IC developments. The two talks are:

- Teaching Experiences
- Some views on teaching IC Design methods

These are followed by a panel discussion which is to address questions on the expansion of EUROCHIP to Eastern Europe. Typical questions which might be addressed are:

- Is it likely?
- What will it be?
- What are the requirements for Poland and EE?
- Programmable logic or full custom?
- Software only or IC fabrication?
- National Scheme?
- Can one start?
- Is administrative structure available?
- Hardware needs?

I wish everyone a pleasant and enjoyable workshop and I am pleased to say there will be time in the programme for us to enjoy the excellent scenery and mountain pursuits that are a part of life in this beautiful part of Poland.